# High Performance of Nonlinear Active Rectifier Voltage and Power

By Saidah Saidah

# High Performance of Nonlinear Active Rectifier Voltage and Power **Factor Control Using Feedback Linearization**

<sup>1,2</sup>Saidah, <sup>1</sup>M. Hery Purnomo, <sup>1</sup>M. Ashari

Abstract - Nonlinear property of Active rectifier is caused by the use of the semiconductor switches, resulting in high harmonic distortion and high ripple out 23 voltage on the DC side. This research develops a nonlinear controller to improve performance of the dc outs t voltage and line current based on feedback linearization. This controller mathematically transform nonlinear system dynamics into fully linear ones, so that linear control techniques can be applied. It is designed with selecting energy function on DC side and quadrature-axis current on AC side as output 6 riables, in order to avoid the internal dynamics, so that the system becomes stable and robust. The system applies switch function of time as the input of control strategy, instead of current and voltage in d-q frame. The proposal is validated through different tests. These tests include difference performance with conventional controller (PI optimized). The simulation results in that the output voltage performing without any overshoot, while the ripple and settling time is 0.006 sec, which is better result compared to PI optimized.

Keywords: Active Rectifier, Feedback Linearization, PI Optimized.

### Nomenclature

| $V_{dc}$                                 | DC voltage (V)                   |
|--|----------------------------------|
| S  | Switch function                  |
| i  | Line current (A)                 |
| e  | Source line voltage (V)          |
| R  | Line resistance $(\Omega)$       |
| L  | Line inductance (H)              |
| $i_{\rm L}$                              | Load current (A)                 |
| $C_{dc}$                                 | DC capasitance (F)               |
| $\mathbf{w}_{\mathrm{c}}$                | DC energy function (Joule)       |
| $e_{\rm w}$                              | DC tracking error                |
| $e_q$                                    | quadratic current tracking error |
| $k_{11}, k_{12}, k_{13}, k_{21}, k_{22}$ | gain                             |

Subscript Superscripts and Operators Reference value

Direct, quadrature axis component d, q a, b, c phasa a, phasa b, phasa c

### I. Introduction

Active Rectifier is widely used in many power electronics fields. It has feature such as providing 12 stant Active Rectifier is widely used in many DC bus voltage, low harmonic distortion of utility currents, high power factor and bidirectional power flow. Due to these features, the active rectifier is a good choice for application in industrial drives [1],[2]. The active rectifier is nonlinear system. In general, this nonlinear impacts are not expected (for example, ripple on DC

voltage, harmonics in the line current and the low value of power factor) so that is required proper controller that can compensate them. Many researches focus on the control principle of active rectifier of dealing nonlinearity property. 26 example, two of classical linear PID controllers to regulate the DC voltage and the harmonic of current line. PID controllers were considered in simple and robust techniques [3[-[5]. However, one of the drawbacks in using PID control techniques was that they were not sufficient to obtain the desired tracking control performance because of the nonlinearity of the active rectifier. Other researchers have applied the method known as fuzzy logic controller and Neural networks [6]-[8]. Fuzzy Logic is one of nonlinear controller but it is unstructure controller, vice versa active rectifier is structured system. Researchers also have developed several nonlinear controllers in order to control Active Rectifier. Controllers based on Jacobian linearisation around an equilibrium point have been introduced in [9]-[10], however, this strategy presents some drawbacks. Among others, operation range is restricted and a relatively big output capasitor is needed for keeping a constant DC voltage in presence of a varying load, backsteppi 8 technique [11], [12]. Then, nonlinear

approaches also have been proposed by other researchers, that is input output linearization based on feedback 5 earization [13]-[19]. This technique mathematically transform nonlinear system dynamics into fully or partly linear ones, so that linear control techniques can be applied. Then linear control laws can be used for guaranteeing stability in the whole operation range. In this technique depends on selected output variabels. In this paper [15] selects DC voltage and direct current as output

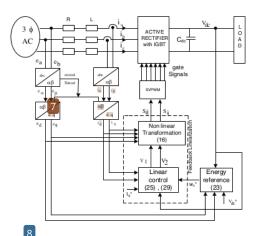
variables, a nonlinear unstable internal dynamics appears. In [16] author have selected difference between the voltage source and the voltage active rectifier on AC side as input variabel, then variabels DC voltage and direct axis current as input. Some many authors have proposed converter energy as output in feedback controller design to accomplish a minimum-phase output and faster DC-volta [19] control [17]-[18].

This paper proposes a nonlinear multivariable control technique based on feedback linearization theory with selecting energy function on DC side and quadrature-axis current on AC side as output variables. One of the primary advantages of this nonlinear controller can avoid internal dynamics and zero dynamics. For this reason, the proposed technique avoids the draw 24k appearing in other proposals due to the coupling between the inner current loop and 21 outer voltage loop.

The paper is organized as follows. In section II, the proposed control strategy is presented. The mathematical model expressed in a d-q synchronous reference frame of the Active Rectifier was derived in section III. Design of the feedback linearization control strategy that consist of non linear transformation and design of linear control have developed in section IV. The performance evaluation, simulation results and compute with conventional controller (PI optimized) are presented in section V. Finally, conclusion are drawn in section VI.

### II. Proposed Control Strategy

The proposed system uses dynamic model of active rectifier in a synchronous d-q reference frame  $\frac{1}{2}$  he system applies switch function in d-q frame ( $s_d$ ,  $s_q$ ) as the input of control strategy, instead of current and voltage.



8 Fig. 1. Block Diagram of the Proposed Control Strategy

In Fig. 1, an energy reference block is used to produce the energy reference of the  $25^{\circ}$  side  $(w_c^*)$  that is calculated by using equation (23). The DC voltage of the active rectifier  $(V_{dc})$  can be controlled through this energy function. The linear control block shown is obtained from

equations tracking control (25) and (29), so producing the new control inputs  $v_1$  and  $\,v_2$ . The nonlinear control block in Fig.1 is used for transforming  $v_1,\,v_2$  become  $s_d$ ,  $s_q$  by using control law in equation (16).

### III. Modelling of The System

A three-phase mathematical model for the active rectifier 22 derived in [20]-[21]. Fig. 2 represents the topology of the three phase active rectifier.

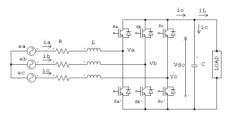


Fig. 2. The topology of the three-phase active rectifier system

the active rectifier can be modeled as

$$L\frac{di_{a}}{dt} + Ri_{a} = e_{a} - V_{dc} \left( S_{a} - \frac{1}{3} (S_{a} + S_{b} + S_{c}) \right)$$

$$L\frac{di_{b}}{dt} + Ri_{b} = e_{b} - V_{dc} \left( S_{b} - \frac{1}{3} (S_{a} + S_{b} + S_{c}) \right)$$

$$L\frac{di_{c}}{dt} + Ri_{c} = e_{c} - V_{dc} \left( S_{c} - \frac{1}{3} (S_{a} + S_{b} + S_{c}) \right)$$

$$C\frac{dV_{dc}}{dt} = \left( i_{a}S_{a} + i_{b}S_{b} + i_{c}S_{c} \right) - i_{L}$$
(2)

The model presented in (1) and (2) is transformed into a synchronous d-q reference frame, resulting in:

$$\dot{Li_d} = -Ri_d - L\omega i_q - s_d V_{dc} + e_d$$
 (3)

$$L\dot{i}_{q} = -Ri_{q} + L\omega i_{d} - s_{q}V_{dc} + e_{q}$$
(4)

$$C_{dc}\dot{V}_{dc} = \frac{3}{2} \left( s_{d}i_{d} + s_{q}i_{q} \right) - \frac{V_{dc}}{R_{L}} \tag{5}$$

Where  $\omega$  is the angular frequency of the source voltage and  $R_L$  is resistive load that connected to the output terminal.

Equation (3) – (5) are then reconstructed considering a multi-input multi-output (MIMO) system as follows [22]:

$$\dot{\mathbf{x}} = \mathbf{f}(\mathbf{x}) + \mathbf{g}(\mathbf{x})\mathbf{u} \tag{6}$$

$$\mathbf{v} = \mathbf{h}(\mathbf{x}) \tag{7}$$

where

x is the state vector;

u is control input vector;

y is vector of system outputs;

f, and g are smooth vector fields;

h is smooth scalar

The vectors are defined as follows:

$$\dot{x} = \begin{bmatrix} i_d \\ i_q \\ \dot{V}_{dc} \end{bmatrix} ; f(x) = \begin{bmatrix} -\frac{R}{L}i_d - \omega i_q + \frac{e_d}{L} \\ -\frac{R}{L}i_q + \omega i_d + \frac{e_q}{L} \\ -\frac{V_{dc}}{R_L C_{dc}} \end{bmatrix}$$

$$g(x) = \begin{bmatrix} -\frac{V_{dc}}{L} & 0\\ 0 & -\frac{V_{dc}}{L}\\ \frac{3i_{d}}{2C_{dc}} & \frac{3i_{q}}{2C_{dc}} \end{bmatrix} ; u = \begin{bmatrix} S_{d}\\ S_{q} \end{bmatrix}$$
(8)

The active rectifier has two output variables. First ouput variable is energy function on DC side  $(w_c)$ . Through this function, the DC voltage of active rectifier can be controlled. The second output variable is Quadrature-axis current  $(i_q)$ . This ouput is used to control power factor and harmonic distortion. By choosing these outputs, the total of relative degree of is three that equals to order of the active rectifier, so that, the system can be avoided from internal dynamic, then complete feedback linearization is obtained and two linear decoupled loops are to be controlled.

The variables of system output are

$$y = \begin{bmatrix} w_C \\ i_q \end{bmatrix} \quad ; \qquad h(x) = \begin{bmatrix} \frac{L}{2} (i_d^2 + i_q^2) + \frac{C_d c}{3} V_{dc}^2 \\ i_q \end{bmatrix} \quad (9)$$

Below is the description to find the energy function of equation (9). Energy is obtained from power balance equation relating the AC side power  $P_{ac}$  and the DC side power  $P_{dc}$ 

$$P_{dc} = P_{ac}$$

$$V_{dc}i_{o} = \frac{3}{2} (s_{d}V_{dc}i_{d} + s_{q}V_{dc}i_{q})$$
(10)

Assuming the net power in the DC side is stored in the capasitor, then the output current  $i_0$  is replaced with capasitor current  $C_{d_k} \frac{dV_{d_k}}{dt}$ . The equation (10) can be

rewritten is

$$C_{dc} \frac{dV_{dc}}{dt} = \frac{3}{2} \left( i_d S_d + i_q S_q \right) \tag{11}$$

From the dynamic model of the active rectifier, the entire power is calculated from equation (3) and (4) multiplied with i<sub>d</sub> and i<sub>q</sub> respectively

$$\operatorname{Li}_{d} \dot{\mathbf{i}}_{d} = -\operatorname{Ri}_{d}^{2} - \operatorname{L}\omega \mathbf{i}_{d} \mathbf{i}_{d} - \mathbf{s}_{d} \mathbf{V}_{dc} \mathbf{i}_{d} + \mathbf{e}_{d} \mathbf{i}_{d} \tag{12}$$

$$\text{Li}_{a}\dot{i}_{a} = -\text{Ri}_{a}^{2} + \text{L}\omega i_{d}i_{a} - s_{a}V_{dc}i_{a} + e_{a}i_{a}$$
 (13)

By summing equation (11), (12) and (13), a new equation of power is found as follows:

$$\left\{ i_{d}L\frac{di_{d}}{dt} + i_{q}L\frac{di_{q}}{dt} + \frac{2}{3}V_{dc}C_{dc}\frac{dV_{dc}}{dt} \right\} = i_{d}e_{d} + i_{q}e_{q} - R(i_{d}^{2} + i_{q}^{2})$$

The left hand side of equation (14) is a power function (14) in derivative model, integration of this function results in a energy function. Where

$$w_{c} = \int i_{d}L \frac{di_{d}}{dt} + i_{q}L \frac{di_{q}}{dt} + \frac{2}{3}V_{dc}C_{dc} \frac{dV_{dc}}{dt}$$
 (15)

$$w_{C} = \frac{L}{2} \left( i_{d}^{2} + i_{q}^{2} \right) + \frac{1}{3} C_{dc} V_{dc}^{2}$$
 (16)

### IV. Design of Feedback Linearization Controller

There are two steps to design the Feedback Linearization control: nonlinear transformation into a linear system and design of linear controller

### IV.1 Non Linear Transformation

Nonlinear transformation is a process to generate a 10 ar input-output relation for a nonlinear active rectifier. An approach to obtain the input-output linearization of the MIMO system is to differentiate each output of the system until the inputs appear. By differentiating each output y in equation (9) by formula.

$$\dot{\boldsymbol{y}}_{i} = \boldsymbol{L}_{f} \, \boldsymbol{h}_{i} + \sum_{j=1}^{2} \! \left( \boldsymbol{L}_{gj} \boldsymbol{h}_{i} \, \right) \! \boldsymbol{u}_{j}$$

Where  $L_f h$  and  $L_g h$  represent Lie derivatives of h(x) with respect to f(x) and g(x), respectively. If  $L_{gj}h_i(x)=0$  for all j, then the inputs do not appear and we have to differentiate repetively as

$$y_i^{(r_i)} = L_f^{r_i} h_i + \sum_{i=1}^{2} (L_{gj} L_f^{r_{i-1}} h_i) u_j$$

With 
$$\begin{bmatrix} y_1^{(r_i)} \\ y_2^{(r_i)} \end{bmatrix} = \begin{bmatrix} v_1 \\ v_2 \end{bmatrix}$$
 for at least one j.

If we perform the above procedure for each output  $y_i$  will be obtained as follows

$$\begin{bmatrix} \ddot{\mathbf{w}}_{c} \\ \dot{\mathbf{i}}_{q} \end{bmatrix} = \mathbf{A}(\mathbf{x}) + \mathbf{E}(\mathbf{x}) \begin{bmatrix} \mathbf{S}_{d} \\ \mathbf{S}_{q} \end{bmatrix}$$

Where

$$A(x) = \begin{bmatrix} L_f^2 h_1 \\ L_f h_2 \end{bmatrix} = \begin{bmatrix} \frac{2R^2}{L} \left( i_d^2 + i_q^2 \right) + \frac{e_d}{L} \left( e_d - 3Ri_d - \omega Li_q \right) + \frac{4}{3} \frac{V_{dc}^2}{R_L^2 C_{dc}} \\ -\frac{R}{L} i_q + \omega i_d \end{bmatrix}$$
 
$$E(x) = \begin{bmatrix} L_{g1} L_f h_1 & L_{g2} L_f h_1 \\ L_{g1} h_2 & L_{g2} h_2 \end{bmatrix}$$
 
$$E(x) = \begin{bmatrix} \left( \frac{2R}{L} i_d V_{dc} - \frac{e_d V_{dc}}{L} - \frac{2}{R_L C_{dc}} i_d V_{dc} \right) \left( \frac{2R}{L} i_q V_{dc} - \frac{2}{R_L C_{dc}} i_q V_{dc} \right) \\ 0 & \left( -\frac{V_{dc}}{L} \right) \end{bmatrix}$$
 
$$W_C^* = \frac{L}{2} \left( \sqrt{\left( \frac{e_d}{2R} \right)^2 + \frac{2}{3R} \frac{V_{dc}^2}{R_L}} + \frac{e_d}{2R} \right)^2 + \frac{C_{dc}}{3} V_{dc}^* \quad (23)$$
 Equation (23) depicts that the DC voltage reference  $V_{dc}^*$  should be determined. The design of the tracking controller for the input  $v_1$  is given by 
$$v_1 = \ddot{w}_c^* - k_{11} \dot{e}_w - k_{12} e_w \quad (24)$$
 Where  $e_w = w_c - w_c^*$  and the tracking error of the closed loop system as follows 
$$\ddot{e}_w + k_{11} \dot{e}_w + k_{12} e_w = 0 \quad (25)$$

The matrix E(x) is called the decoupling matrix for the  $\overline{\text{MIMO}}$  system. If the E(x) is nonsingular, then the input transformation can be obtained as

$$\begin{bmatrix} \mathbf{S}_{\mathbf{d}} \\ \mathbf{S}_{\mathbf{q}} \end{bmatrix} = \mathbf{E}^{-1}(\mathbf{x}) \begin{pmatrix} \mathbf{v}_{1} \\ \mathbf{v}_{2} \end{bmatrix} - \begin{bmatrix} \mathbf{A}_{1}(\mathbf{x}) \\ \mathbf{A}_{2}(\mathbf{x}) \end{bmatrix}$$
 (18)

$$E^{-1}(x) = \frac{1}{\frac{V_{dc}}{L} \left( e_d + \frac{2Li_d}{R_L C_{dc}} - 2Ri_d \right)} \begin{bmatrix} -1 & (\frac{2L}{R_L C_{dc}} i_q - 2Ri_q) \\ 0 & \left( 2Ri_d - e_d - \frac{2L}{R_L C_{dc}} i_d \right) \end{bmatrix}$$

Substituting (18) into (17) results in a linear differentia relation between the output y and the new input v

$$\begin{bmatrix} \ddot{\mathbf{w}}_{\mathbf{c}} \\ \dot{\mathbf{i}}_{\mathbf{q}} \end{bmatrix} = \begin{bmatrix} \mathbf{v}_{1} \\ \mathbf{v}_{2} \end{bmatrix} \tag{19}$$

### IV.2 Design of Linear Control

After linear relation in equations (19) are obtained, then the controller for the linearized system can be designed. The DC voltage of active rectifier (Vdc) is controlled through energy function (wc), then power factor and harmonic distortion is controlled through output Quadrature-axis current (i<sub>0</sub>).

### DC Voltage Control:

According Fig. 1. energy reference block (w<sub>Cref</sub>) is produced from equations: energy definition (20), power balance (21) and unity power factor (22)

$$w_{C}^{*} = \frac{L}{2} \left( i_{d}^{*2} + i_{q}^{*2} \right) + \frac{C_{dc}}{3} V_{dc}^{*2}$$
 (20)

$$\dot{w}_{C}^{*} = \frac{2}{3} \frac{V_{dc}^{*2}}{R_{I}} + e_{d} i_{d}^{*} - R \left( i_{d}^{*2} + i_{q}^{*2} \right) = 0$$
 (21)

$$i_{q}^{*} = 0$$
 (22)

Substituting equation (20), (21) and (22) results in an energy reference as follow

$$w_{C}^{*} = \frac{L}{2} \left( \sqrt{\left(\frac{e_{d}}{2R}\right)^{2} + \frac{2}{3R} \frac{V_{dc}^{2}}{R_{L}}} + \frac{e_{d}}{2R} \right)^{2} + \frac{C_{dc}}{3} V_{dk}^{*}$$
 (23)

$$v_1 = \ddot{w}_c^* - k_{11}\dot{e}_w - k_{12}e_W \tag{24}$$

Where  $e_w = w_c - w_c^*$  and the tracking error of the closed loop system as follows

$$\ddot{e}_{w} + k_{11}\dot{e}_{w} + k_{12}e_{w} = 0 \tag{25}$$

To guarantee robustness and stableness against parametric uncertainty is added an integral error, so that (24) and (25) become

$$v_1 = \ddot{w}_c^* - k_{11}\dot{e}_w - k_{12}e_w - k_{13}\int e_w dt$$
 (26)

$$\ddot{e}_{w} + k_{11}\dot{e}_{w} + k_{12}e_{w} + k_{13}\int e_{w}dt = 0$$
 (27)

Fig. 3 shows block diagram of linear control for the DC voltage

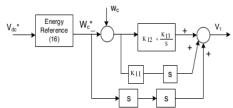


Fig. 3. Block diagram of the DC voltage control

### Power Factor [14] Harmonic Distortion Control

To produce unity power factor and low harmonic distortion, reference value of quadratic-axis current is set  $\operatorname{zero}\left(i_{q}^{*}=0\right).$ 

For tracking control, the new control input is given by

$$\mathbf{v}_{2} = \dot{\mathbf{i}}_{a}^{*} - \mathbf{k}_{21}\mathbf{e}_{a}$$
 (28)

 $e_q = i_q - i_q^*$ , yields an exponentially stable Where tracking error

Then, the output errors become

$$\dot{\mathbf{e}}_{q} + \mathbf{k}_{21} \mathbf{e}_{q} = 0 \tag{29}$$

To guarantee robustness and stableness against parametric uncertainty is added an integral error, so that (28) and (29) become

$$v_2 = i_q^* - k_{21}e_q - k_{22}\int e_q dt \tag{30}$$

$$\dot{e}_{q} + k_{21}e_{q} + k_{22}\int e_{q}dt = 0$$
 (31)

Fig. 4 shows block diagram of linear control for the power factor and harmonic distortion

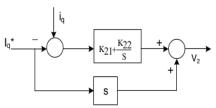


Fig. 4. Block diagram of power factor control

The gains kij are can be designed by Pole Planement or linear quadratic regulator techniques, so that asymptotic tracking control to the reference is achieved. Fig. 5. show blok diagram of feedback Linearization.

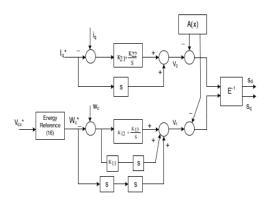


Fig. 5. Block diagram of Feedback Linearization Control

### PI Optimized

An PI optimized system as shown in Fig. 6. is used to compare the performance of active rectifier with feedback linea ation. The design of PI Optimized was aimed to tune the gains of the PI controller (Kp and Ki) and to optimize the response of the plant. The result simulation by using PI-optimized showed that proportional constant (Kp) was 3.18 and integral constant (Ki) was 5.024.

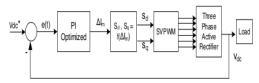


Fig. 6. Block diagram of PI Optimized controller

### V. Result and Discussion

The proposed controller was evaluated through several simulations. This section present the most relevan results. The controller performance was tested through simulations on a system which system parameter can be seen in table 1.

TABLE I SYSTEM PARAMETER

| Parameters                    | Value                 |
|-------------------------------|-----------------------|
| DC Voltage (V <sub>dc</sub> ) | 650 volt              |
| Amplitudo Source Voltage      | $\sqrt{2}$ x 220 volt |
| Fundamental Frequency         | 50 Hz                 |
| Line Resistance               | 0.3 Ω                 |
| Line Inductor                 | 8 mH                  |
| Filter capasitor              | 1000 uF               |
| Frequency Switching           | 5000 Hz               |

Simulation results using the Feedback Linearization controller were compared to simulation using a PI optimized. The purpose of this comparison was to obtain high performance of the active rectifier system due to nonlinear effects.

Fig 7. presents the DC voltage of active rectifier with PI Optimized and Feedback Linearization controller. The PI optimized control methode produce the DC voltage with overshoot (Mp = 2%) and ripple 5.64 %., while the feedback linearization controller without overshoot and ripple with setling time 0.006 sec.

Fig.8. Demonstrates the line phase voltage and current. Fig 8a. the simulation results showed that line current produces transients respon until 0.1 sec , Total Harmonic Distortion (THD) = 10.42 % , Power factor = .0.92 for PI optimized. Fig. 8b. line current reaches steady state faster, THD = 0.21 % , Power factor = 1 for feedback linearization.

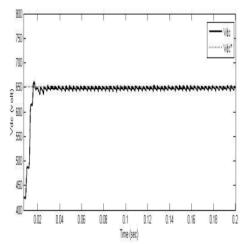
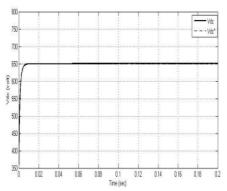


Fig.7.(a). Presents the DC voltage with PI Optimized



(b). The DC voltage with feedback linearization

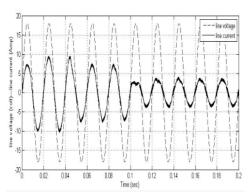


Fig. 8a. Demonstrates the line phase voltage and current of PI optimized

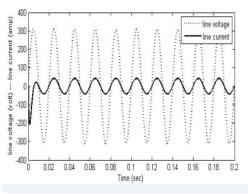


Fig. 8b. Demonstrates the line phase voltage and current of the proposed control

Fig 9. show the behavior under unbalanced conditions of the feedback linearization controller. An unbalance voltage with reducing amplitudo input voltage phasa A become 85% from the nominal voltage. Fig. 9a shows the active rectifier input voltages when the unbalance. Fig. 9b shows the DC voltage in the

unbalance where is clearly seen small ripple voltage, however unity power factor is maintained (0.973).

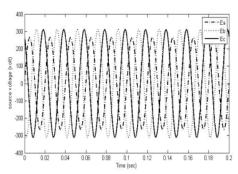


Fig. 9a shows the active rectifier input voltage

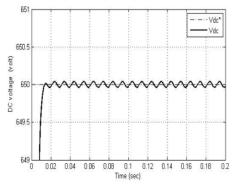


Fig. 9b. the DC voltage under the unbalance condition

## VI. Conclusion

This paper presented high performance of nonlinear active rectifier using feedback linearization. The nonlinearity of the modeled system was eliminated by the proposed nonlinear control scheme such that avoided from internal dynamic so the linear control law can be us 14 to control the system. This paper also presented to compare the simulation results with PI optimized. The simulation results showed that the feedback linearization controller had the best characteristics compared to PI optimized control methods, especially the DC voltage, power factor and THD in dealing with the nonlinearity behaviour..

### References

- Hong-Seok Song, In-Won Joo, and Kwanghee Nam, "Source Voltage Sensorless Estimation Scheme for PWM Rectifiers Under Unbalanced Conditions", *IEEE Transactions On Industrial Electronics*, Vol. 50, No. 6, December 2003.
- [2] Ashari, M., W.W.L. Keerthipala and C.V. Nayar, "A Single Phase Parallely Connected Uninterruptible Power Supply/ Demand Side Management System", *IEEE Transactions on Energy Conversion*, vol. 15, No. 1, March 2000, pp.97-102.

- [3] S. R. Hadian-Amrei and H. Iranmanesh, "Novel Direct Power Control for Compensating Voltage Unbalance and Load Fluctuations in PWM Rectifiers", ACSE Journal, Volume (6), Issue (4), Dec., 2006.
- [4] P. Antoniewicz<sup>n</sup> And M.P. Ka Zmierkowski, "Predictive direct power control of three-phase boost Rectifier", Bulletin Of The Polish Academy Of Sciences Technical Sciences Vol. 54, No. 3, 2006
- [5] Mariusz Malinowski and Marian P. Kazmierkowski, "Control of Three-Phase PWM Rectifier – A Comparative Review", IEEE Industrial Electronics Society Newsletter Issn 0746-1240 Vol. 51, No. 1 2004
- [6] Masoud Hajihashemi, Ali Nazeran Motlagh, "Improved 3-phase PWM Boost Rectifiers in Unbalanced Network by Fuzzy Controller", The International Conference on Electrical Engineering 2009.
- [7] C. Cecati, A. Dell'Aquila, A. Lecci, and M. Liserre, "Implementation issues of a fuzzy-logic-based three-phase active rectifier employing only voltage sensors," *IEEE Transactions on Industrial Electronics*, vol. 52, pp. 378–385, April 2005
- [8] Wenxin Liu, Li Liu, David A. Cartes, Xin Wang, "Neural Network Based Controller Design for Three-Phase PWM AC/DC Voltage Source Converters" 2008 IEEE
- [9] R. S. Pena, R. J. Cardenas, J. C. Clare, and G. M. Asher, "Control strategies for voltage control of a boost type PWM converter," *IEEE 32nd Power Elec. Spec. Conf. PESC'01*, vol. 2, pp. 730– 735, June 2001.
- [10] Blasco and V. Kaura, "A new mathematical model and control of a three-phase AC-DC voltage source converter," *IEEE Transactions on Power Electronics*, vol. 12, pp. 116–123, January 1997.
- [11] A. Hadri Hamida, A. Allag, M.Y. Hammoudi dll, "A Nonlinear Adaptive Backstepping Approach Applied to A Three phase PWM AC – DC Converter Feeding Induction Heating", Elsevier : Communications in Nonlinear Science and Numerical Simulation, April 2009
- [12] M.-T. Tsai and W. I. Tsai, "Analysis and design of three-phase AC-to- DC converters with high power factor and near-optimum feedforward," *IEEE Trans. on Ind. Electr.*, vol. 46, pp. 535–543, June 1999
- [13] B. Yin, R. Oruganti, S. K. Panda, and A. K. S. Bhat, "Control of a threephase PWM rectifier based on a dual single-input singleoutput linear model," *Inter. Conf. on Power Electr. and Drives* Systems, PEDS'05, vol. 1, pp. 456–461, January 2006.
- [14] Loubna Yacoubi\*, Farhat Fnaiech1, Louis-A. Dessaint, Kamal Al-Haddad, "New nonlinear control of three-phase NPC boost rectifier operating under severe disturbances", Elsevier: Mathematics and Computers in Simulation 63 (2003).
- [15] Tzann-Shin Lee, Nat. Lien-Ho, "Input-output linearization and zero-dynamics control of three-phase AC/DC voltage-source converters", Power Electronics, IEEE Transaction on Jan 2003.
- [16] Dong-Choon Lee, G-Myoung Lee, and Ki-Do Lee, "DC-Bus Voltage Control of Three-Phase AC/DCPWM Converters Using Feedback Linearization", IEEE Transactions On Industry Applications, VOL. 36, NO. 3, MAYJUNE 2000.

- [17] Bin Lu and Boon-Teck Ooi, Nonlinear Control of Voltage-Source Converter Systems", IEEE TRANSACTIONS ON POWER ELECTRONICS, VOL. 22, NO. 4, JULY 2007
- [18] Gensior A., Rudolph J., Guldner H.: 'Flatness based control of three-phase boost rectifiers'. European Conf. on Power Electronics and Applications EPE'05, September 2005.
- [19] Monia Charfeddine, Khalil Jouili, Houssem Jerbi, Naceur Benhadj Braiek, 'Exact Input-Output Linearization, Robust Relative Degree, Lyapunov Function, Trajectory Tracking, IREE Journal, Vol. 3. n. 2, pp. 219-226, April 2010.
- [20] pp. 1–9R. Wu, S. B. Dewan, and G. R. Slemon, "A PWM act odc converter with fixed switching frequency," in Conf. Rec. 1988 IEEE-IAS Ann Meeting, pp. 706–711.
- [21] R. Wu, S. B. Dewan, and G. R. Slemon, "Analysis of an ac-to-dc voltage source converter using PWM with phase and amplitude control," *IEEE Trans. Ind. Applicat.*, vol. 27, no. 2, pp. 355–364, Mar./Apr. 1991.
- [22] J.-J. E. Slotine and W. Li, Applied Nonlinear Control. Englewood Cliffs, NJ: (Prentice-Hall, 1991, pp. 207–271).

### Authors' information

<sup>1</sup>Department of Electrical Engineering Sepuluh Nopember Institute of Technology (ITS), Surabaya-Indonesia <sup>2</sup>Department of Electrical Engineering Bhayangkara University, Surabaya-Indonesia

E-mail: \(^1\)sdhbaisa61@yahoo.com, \(^2\)hery@ee.its.ac.id, \(^3\)ashari@ee.its.ac.id

Saidah received her bachelor and master degree from Institut Teknologi



Sepuluh Nopember (ITS) Surabaya in 1985 and 2005 respectively. She is currently a Ph.D student at Electrical 7 gineering Dept. of ITS. Her research interest on use of artificial intelligent for power electronics, control and electric drives applications.

Prof. Mauridhi Hery Purnomo received his bachelor degree



from Institut Teknologi Sepuluh Nopember (ITS) in 935. In 1995 and 1997, he got his Master and Ph.D degrees from Osaka University, Japan. He joined ITS as 9 turer in 1985 and became Professor in 2003. His current interests include intelligent system applications, image processing, medical imaging, control and management. He is a Member of IEEE.

**Prof. Mochamad Ashari** recieved his bachelor degree from (ITS) in 1989 and joined ITS as lecturer in 1990. In 1998 and 2001, he got his Master and PhD degrees from Curtin University, Australia. He



joined ITS as lecturer in 1989 and became Professor in 2009. His research field is industrial electronics and applications, including harmonics filter design, solar home systems, etc. He has received many research grants from ADB, JICA, and Indonesian Goverment.

Copyright © 2007 Praise Worthy Prize S.r.l. - All rights reserved

International Review of Electrical Engineering, Vol. 8, n. 2

# High Performance of Nonlinear Active Rectifier Voltage and Power

| ORIGI | INAL | .ITY | REP | ORT |
|-------|------|------|-----|-----|

| 2     | 0 | % |
|-------|---|---|
| CIMIL |   |   |

| SIMILARITY INDEX |                            |                       |  |
|------------------|----------------------------|-----------------------|--|
| 1                | ynucc.yeungnam.ac.kr       | 71 words — <b>2</b> % |  |
| 2                | www.semanticscholar.org    | 48 words $-2\%$       |  |
| 3                | raiith.iith.ac.in Internet | 43 words — <b>1</b> % |  |
| 4                | jpels.org<br>Internet      | 39 words — <b>1</b> % |  |
| 5                | docplayer.net Internet     | 34 words — <b>1</b> % |  |
| 6                | jatit.org<br>Internet      | 34 words — <b>1</b> % |  |
| 7                | erepository.uwks.ac.id     | 33 words — <b>1</b> % |  |
| 8                | dokumen.pub<br>Internet    | 32 words — <b>1</b> % |  |
| 9                | repository.unhas.ac.id     | 29 words — <b>1</b> % |  |

| www.cder.dz Internet              | 26 words — <b>1 %</b>  |
|-----------------------------------|--|
| ijircce.com<br>Internet           | 25 words — <b>1</b> %  |
| pt.scribd.com<br>Internet         | 25 words — <b>1</b> %  |
| livrepository.liverpool.ac.uk     | 20 words — <b>1</b> %  |
| dspace.lboro.ac.uk Internet       | 18 words — <b>1</b> %  |
| Ird.yahooapis.com Internet        | 17 words — <b>1 %</b>  |
| research.sabanciuniv.edu Internet | 15 words — < 1 %   |
| www.iaeng.org                     | 15 words — < 1 %   |
| koreascience.or.kr Internet       | 14 words — < 1 %   |
| www.waset.org Internet            | 13 words — < 1 %   |
| www.yarbis1.yildiz.edu.tr         | 13 words — < 1 %   |
| era.library.ualberta.ca           | 10 words — < 1 %   |
|                                   | ijircce.com Internet  pt.scribd.com Internet  livrepository.liverpool.ac.uk Internet  dspace.lboro.ac.uk Internet  Ird.yahooapis.com Internet  research.sabanciuniv.edu Internet  www.iaeng.org Internet  koreascience.or.kr Internet  www.waset.org Internet  www.yarbis1.yildiz.edu.tr Internet  era.library.ualberta.ca |

cmpe.emu.edu.tr

 $_{8 \text{ words}}$  - < 1 %

24 idoc.pub

8 words — < 1 %

ies.ieee-ies.org

 $8 \, \text{words} \, - < 1 \, \%$ 

scholarbank.nus.edu.sg

 $_{8 \text{ words}}$  - < 1%

EXCLUDE QUOTES

ON

**FXCLUDE MATCHES** 

OFF

EXCLUDE BIBLIOGRAPHY ON