

YAYASAN BRATA BHAKTI JAWA-TIMUR UNIVERSITAS BHAYANGKARA SURABAYA LEMBAGA PENELITIAN DAN PENGABDIAN KEPADA MASYARAKAT (LPPM)

Jalan Ahmad Yani 114 Surabaya, Telp. 031-8285602, 8291055, Fax. 031-8285601

SURAT KETERANGAN Nomor : Sketl (1/2023/LPPM/UBHARA

Yang bertanda tangan dibawah ini :

Nama	: Drs. Heru Irianto, M.Si
NIP	: 9000028
NIDN	: 0714056102
Jabatan	: Kepala Lembaga Penelitian dan Pengabdian Kepada Masyarakat
	(LPPM) Universitas bhayangkara Surabaya

Dengan ini menerangkan bahwa dosen prodi Teknik elektro Universitas Bhayangkara Surabaya atas nama **Dr. Ir. Saidah, MT** benar telah melakukan kegiatan :

- Menulis artikel yang telah di publish di Jurnal Internasional Telkomnika, Volume 10, Nomor 4 berjudul Ambient light adaptive LED light dimmer, sebagai koresponden, Desember 2019, ISSN : 2088-8694, terindeks Scopus Q3
- Telah melakukan korespondensi melalui email dan OJS dalam proses review dan penerbitan jurnal tersebut. Bukti korespondensi email dan bukti pendukung adalah benar sudah dilakukan oleh yang bersangkutan serta sudah dilampirkan bersama surat ini.

Demikian surat keterangan ini dibuat untuk dapat dipergunakan sebagaimana mestinya.



ARTIKEL DITERIMA PADA ICW CONFERENCE



Saidah Ubhara <saidah@ubhara.ac.id>

[TELKOMNIKA] Editor Decision - (Link for Registration) --> bit.ly/icwtelkomnika2018

icw.telkomnika@journal.uad.ac.id <icw.telkomnika@journal.uad.ac.id> Kepada: saidah@ubhara.ac.id 6 Agustus 2018 pukul 14.58

--- International Conference and Workshop on TELKOMNIKA (Telecommunication Computing Electronics and Control) 2018

--- September 18-21, 2018 at ROYAL AMBARRUKMO Yogyakarta

--- Jl. Laksda Adisucipto No.81, Ambarukmo, Caturtunggal, Depok, Sleman

--- Yogyakarta 55281, Indonesia

Dear Prof/Dr/Mr/Mrs Authors:

We have reached a decision regarding your submission entitled **"ADAPTIVE LIGHT DIMMER" Paper ID # 10114** to International Conference and Workshop on TELKOMNIKA (Telecommunication Computing Electronics and Control) 2018 (ICW TELKOMNIKA 2018).

Our decision is to ACCEPT with minor revisions.

We invite you to present your paper in the ICW TELKOMNIKA 2018 on Sep 18-20, 2018 in Yogyakarta, Indonesia. All presented papers (after revisions based upon feedback at the conference & workshop, if necessary) will be published in the TELKOMNIKA Telecommunication Computing Electronics and Control (a Scopus indexed journal, SCImago Journal Rank (SJR) Q3) as REGULAR issue. All presented papers will be prioritized for publication in our journal, but all submitted papers still will be processed for possible publication if the papers are suitable.

In preparing your final camera ready paper for conference, you should pay attention to:

1. Authors should have made substantial contributions to:

(a) the conception and design of the study, or acquisition of data, or analysis and interpretation of data

(b) drafting the article or revising it critically for important intellectual content

2. Introduction section

Explain the context of the study and state the precise objective

An Introduction should contain the following three parts:

- Background: Authors have to make clear what the context is. Ideally, authors should give an idea of the state-of-the art of the field the report is about.

- The Problem: If there was no problem, there would be no reason for writing a manuscript, and definitely no reason for reading it.

So, please tell readers why they should proceed reading. Experience shows that for this part a few lines are often sufficient.

- The Proposed Solution: Now and only now! - authors may outline the contribution of the manuscript. Here authors have to make sure readers point out what are the novel aspects of authors work. Authors should place the paper in proper context by citing relevant papers. At least, 5 references (recently journal articles) are used in this section.

3. Results and Discussion

The presentation of results should be simple and straightforward in style. This section reports the most important findings, including results analyses as appropriate and comparisons to other research results. This section should be supported suitable references.

4. Conclusion

Your conclusion should make your readers glad they read your paper. Summarize sentences the primary outcomes of the study in a paragraph (NOT in numbering).

5. References and Citations

1/31/23, 9:46 PM Email Universitas Bhayangkara Surabaya - [TELKOMNIKA] Editor Decision - (Link for Registration) --> bit.ly/icw-telkomnika2...

We usually expect a minimum of 25 and 40 references primarily to recent journal papers for research/original paper and review paper, respectively. Each citation should be written in the order of appearance in the text [1], [2], [3], [4], (Sequential order!!)

URGENT: SELF CITATION from same author or group authors is MAX 10%.

6. Paragraph

A paragraph is a sentence or group of sentences that support one main idea. Many authors have presented paragraphs in very long terms. Author should use simple sentences which are grammatically correct, but too many can make your writing less interesting.

Every paragraph in a paper should be:

- Unified: All of the sentences in a single paragraph should be related to a single controlling idea (often expressed in the topic sentence of the paragraph).

- Clear: The sentences should all refer to the central idea of the paper.

Coherent: The sentences should be arranged in a logical manner and should follow a definite plan for development.
 Well-developed: Every idea discussed in the paragraph should be adequately explained and supported through evidence and details that work together to explain the paragraph's controlling idea.

IMPORTANT:

Please register & submit your final camera paper in MS Word file format (or LATEX source files; ZIP your files if you present your final paper in LATEX) through bit.ly/icw-telkomnika2018 (registration form) or goo.gl/q7Hjx3 (registration form) as soon.

Conference fee please refer to bit.ly/icw-poster or goo.gl/3WJWFp

Batch I (for latest papers submission: May 1, 2018)

- Early Bird deadline: Jul 30, 2018

- Registration deadline: Aug 10, 2018

Batch II (for papers submission AFTER May 1, 2018)

- Early Bird deadline: Aug 10, 2018

- Registration deadline: Aug 30, 2018

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Payment is by T/T transfer:

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I look forward for hearing from you

Thank you

Best Regards, Assoc. Prof. Dr. Tole Sutikno General Chair, ICW TELKOMNIKA 2018 Editor-in-Chief, TELKOMNIKA, Scimago Journal Rank (SJR): Q3 email: tole@journal.uad.ac.id

Tentative agenda of the ICW TELKOMNIKA:

--- September 18-21, 2018 at Royal Ambarrukmo Yogyakarta

--- JI. Laksda Adisucipto No.81, Ambarukmo, Caturtunggal, Depok, Sleman

--- Yogyakarta 55281, Indonesia

* Sep 18, 2018 (08.00-21.00): International Conference-Keynote Speeches and Parallel Sessions

* Sep 19, 2018 (08.00-11.45): International Conference (cont.)

* Sep 19, 2018 (13.00-17.00): Tutorial on Humanoid Robot (Prof. Nadia Magnenat Thalmann), Virtual Reality (Prof. Daniel Thalmann) and Intelligent Autonomous Systems (Prof. Er Meng Joo)

1/31/23, 9:46 PM Email Universitas Bhayangkara Surabaya - [TELKOMNIKA] Editor Decision - (Link for Registration) --> bit.ly/icw-telkomnika2...

* Sep 19, 2018 (18.00-21.30): Award Ceremony Program and Gala Dinner

* Sep 20, 2018 (08.00-16.00): Scientific Writing Workshop on TELKOMNIKA Editors and Authors Meeting (TEAM) for upgrading our papers based upon feedback at the conference.

* Sep 21, 2018: One Day Tour Yogyakarta (optional) - Tour to Yogyakarta and vicinity visiting Sultan's Palace or Kraton, Taman Sari Water Castle, Borobudur and Prambanan Temples

Ignore this email, if your paper is mentioned to b accepted by previous notification and you have register to Google Form

BUKTI BAYAR ICW-CONFERENCE



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LEMBAGA PENERBITAN DAN PUBLIKASI ILMIAH (LPPI)

No. 10114

PAYMENT RECEIPT

Received From Organizing Committee of ICW-TELKOMNIKA 2018

Received from	SAIDAH	_
Amount Paid	Rp 4.800.000	_
		7
Purpose of Payment	ICW TELKOMNIKA 2018 PAYMENT	-
	Yogyakarta, 15 August 2018	;
	ZOD 1960	
	Tole Sutikno, M.T., Ph.D. NIY. 60010310	

SERTIFIKAT ICW CONFERENCE



2018 1st INTERNATIONAL CONFERENCE AND WORKSHOP ON **ELECTRICAL, ELECTRONICS AND CONTROL** TELECOMMUNICATION, COMPUTING,

September 18-21, 2018 | Royal Ambarrukmo Hotel, Yogyakarta, Indonesia

CERTIFICATE OF APPRECIATION

is awarded to

Saidah

In recognition and appreciation of your contribution as

Presenter

for paper entitled Ambient Light Adaptive LED Light Dimmer

Anton Yudhana, Ph.D. Conference Chair J. alaes K

Tole Sutikno, Ph.D. Director, LPPI UAD

CANHAN CO

ARTIKEL DITERIMA PADA ICW CONFERENCE DAN AKAN DIPUBLISH DI TELKOMNIKA MELALUI MENTORING DAN REVISI



LEMBAGA PENERBITAN DAN PUBLIKASI ILMIAH UNIVERSITAS AHMAD DAHLAN

Kampus I: Jln. Kapas 9, Semaki, Yogyakarta 55164 Website: http://lppi.uad.ac.id, email: lppi@uad.ac.id

Yogyakarta, September 1, 2018

No. : L6/084/ICW/VIII/2018

To: Saidah Universitas Bhayangkara Surabaya, Indonesia

Dear Sir/Madam,

Your paper #10114 (Ambient Light Adaptive LED Light Dimmer) for ICW-TELKOMNIKA 2018 was evaluated by anonymous referees by single blind peer review for contribution, originality, relevance, and presentation. Congratulations, your paper is **ACCEPTED** for 1st International Conference and Workshop on Telecommunication, Computing, Electronics and Control 2018 (ICW-TELKOMNIKA 2018). This conference will be held in September, 18-21 2018 in Yogyakarta, Indonesia. All presented papers in this conference (after revisions based upon feedback at the conference & workshop, if necessary) will be published in the TELKOMNIKA Telecommunication Computing Electronics and Control (Scopus indexed journal, SJR Q3).

If you have any question, please contact our committee on icw.telkomnika@journal.uad.ac.id cc: tole@journal.uad.ac.id.

Thank you for your contribution to ICW TELKOMNIKA 2018 and we look forward to meeting you on the conference.

Sincerely yours,

PAPER DARI ICW CONFERENCE YANG TERPILIH MASUK JURNAL TELKOMNIKA MELALUI REVISI DAN MENTORING



Saidah Ubhara <saidah@ubhara.ac.id>

[ICW-Telkomnika] INFORMATION ABOUT UPDATE LIST COACHING PAPER

icw.telkomnika@journal.uad.ac.id <icw.telkomnika@journal.uad.ac.id> Kepada: saidah@ubhara.ac.id 19 September 2018 21.36

Dear ICW TELKOMNIKA Presenters,

Tomorrow, on September 20, 2018 will be held **Manuscript Mentoring and Coaching**. You can check your mentor and room in **bit.ly/icwtelkomnika18**, file **UPDATE LIST COACHING PAPER**. Please check it again, because we have little change about the list. Thank you.

Best regards, ICW TELKOMNIKA Committee

REVIEW PAPER



revised paper

2 pesan

Saidah Ubhara <saidah@ubhara.ac.id> Kepada: tole@journal.uad.ac.id 24 Oktober 2018 pukul 16.10

Saidah Ubhara <saidah@ubhara.ac.id>

Dear Tole Sutikno, Ph.D. General Chair, 2018 ICW-TELKOMNIKA Editor-in-Chief, TELKOMNIKA

I send my paper that have revised and File Response to Mentor(s) Comments. Thanks

Best Regards Dr. Saidah

2 lampiran

Paper ICW18 - Revised.docx 656K

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Tole Sutikno <tole@journal.uad.ac.id> Kepada: saidah@ubhara.ac.id 24 Oktober 2018 pukul 17.34

Dear Sir/Madam,

Your email has reached my queues safely, so i will get back in touch with you there as soon as i can. Just a heads up i might not get back to you straight away, as i answer all emails in order to keep things fair.

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Your cooperation is very appreciated.

Thank you

Best Regards, Tole Sutikno Editor: TELKOMNIKA TCEC, http://journal.uad.ac.id/index.php/TELKOMNIKA Email Universitas Bhayangkara Surabaya - revised paper

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- 2. Mr. Son Ali Akbar: sonjannah@gmail.com, Phone: +62 85643888512
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Our events:

International Conference and Workshop on Telecommunication, Computing, Electronics and Control (ICW-TELKOMNIKA), September 18-21, 2018 in Yogyakarta, Indonesia http://journal.uad.ac.id/index.php/TELKOMNIKA/pages/view/conference

2018 5th International Conference on Electrical Engineering, Computer Science and Informatics (EECSI 2018), October 16-18, 2018 in Malang, Indonesia. http://eecsi.org/2018

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LETTER OF ACCEPTANCE



Saidah Ubhara <saidah@ubhara.ac.id>

[TELKOMNIKA] FEBRUARY 2019 Proofreading

1 pesan

TELKOMNIKA Editors and Authors Meeting <team@journal.uad.ac.id> Kepada: saidah@ubhara.ac.id 31 Desember 2018 pukul 15.55

Dear Authors,

We hope this email reach you well. We are from TELKOMNIKA Staff want to inform you that your paper ID 10114 entitled Ambient Light Adaptive LED Light Dimmer already published in February 2019 Issue. Please check the pdf of your paper that attach in here. We suggest you to checking your paper and if the pdf is correct, we will upload it to OJS TELKOMNIKA.

If you have something to tell us about your paper, please reply this email. We will wait your response until **December 30, 2018**. Thank you for your cooperation.

Best regards, TELKOMNIKA Staff

53 24Oct18 31May 10114 Saidah Revised (lila).pdf 1027K

ARTIKEL PADA TELKOMNIKA

Ambient light adaptive LED light dimmer

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Abstract

This paper presents the design of an Adaptive Light Dimmer based on the method of sensingambient light content to adjust lamp's light intensity accordingly, and thus regulating the room's light content. The device is designed to work with renewable energy sources such as wind and solar energy. This would be useful in less developed countries where AC electricity is not well spread and renewable DC sources, such as solar, can be better utilized. It functions by using the TSL2561 light sensor, ATTiny85 microcontroller to output PWM to the LED driver, LT3795 LED driver to output current to an LED and LT3014 LDO to lower the input voltage and power the microcontroller and sensor. The dimmer is designed to work with a 48V input voltage and operate from an input light range of 20 to 100 Lux. Above 100 Lux the light is off and below 20 Lux the light is fully on.

Keywords: adaptive dimmer, LED driver, light dimmer

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1. Introduction

With regard to efficiently using sustainable energy, there is a significant amount of data supporting the energy saving capabilities of light dimming technology. Some state that smart lighting, using sensors and controllers to control lighting, saves between 50% and 70% of energy compared to an uncontrolled lighting system [1-5]. These energy savings have huge impacts, considering that lighting accounts for about 19% of the electrical energy generated worldwide. In commercial buildings, lighting accounts for even more, 30-40% [6-11]. This being said, smart lighting seems to be the next logical step in saving electricity and tackling the problems of world-wide electrification in today's world.

In order to improve smart lighting technology, it makes sense to utilize electrical devices, such as microcontrollers, to increase efficiency and usability of lighting systems. Microcontrollers would be a reasonable choice for controlling light dimmer due to their decreasing costs, versatility, and ease of use [12-14]. In addition to microcontrollers, DC-DC converters are also essential to improving smart lighting systems [15-17]. These converters are the most efficient ways of converting DC power and consequently are used in many stages of power conversion. Without them, electronics, such as lighting systems, would be much more inefficient. Any improvements to these DC-DC converters directly improve the efficiency and performance of smart lighting systems.

There are existing solutions for AC lighting in the form of smart lighting and socket to bulb interfaces [18-22]. Smart lighting uses "smart systems" to control lighting with applications in computers, tablet devices, or smart phones. On a larger scale, they can also be used to control the lighting system of a room, house, or building. They can wirelessly turn on and off lights, control their brightness, set the lights on a timer, and even integrate sensors. Existing socket to bulb interfaces use a sensor in an attempt to dim the bulb appropriately but they do not function very well. An example of an AC powered smart lighting device, called a home light control module (HLCM) and designed by Ying-Wen Bai and Yi-Te Ku, uses passive infrared (PIR) sensors, light sensors, a microprocessor, and an RF module to control light intensity in all the rooms of a house [23]. The system is illustrated in Figure 1. A single HLCM controls one set of luminaires. As a result, multiple microprocessors determine lighting levels, rather than a single central controller. This device uses the PIR sensors to determine the presence of any people in a room, turning off the lights if no one occupies the room. The light sensors determine

438

the room's brightness levels. If outside sources, like daylight, provide enough light, then the luminaires are turned off. Otherwise, the system activates the appropriate amount of luminaires to achieve the desired brightness levels. The device's RF module allows communication between different HLCMs. In the case that brightness levels are insufficient even when all luminaires are on, communication between HLCMs allows an adjacent HLCM to increase the number of lights to activate. This then affects the light intensity of the first device's room. While these components help this device operate with high efficiency, the design presented in this paper needs not contain any unnecessary modules if their inclusion greatly increases purchase costs. For example, there won't be any need to use the RF module if its use produces costs that outweigh its energy benefits. Additionally, the HLCM does not use light dimming technology while the proposed solution is based on light dimming.

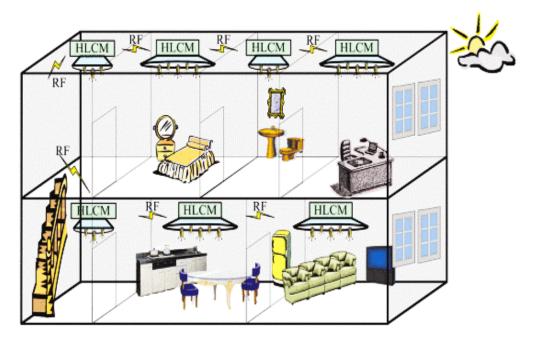


Figure 1. The configuration of an HLCM system for a 2 story house [20]

However, there are currently no existing DC dimmers that are inexpensive and entirely autonomous. The objective of the proposed solution is therefore to design and construct an adaptive DC light dimmer that autonomously dims a set of LEDs depending on the ambient light sensed in the room, providing the appropriate amount of light to the environment it is in. The end product of this project will save electricity usage; thus, reducing electrical costs and increasing the viability of renewable energy.

2. Design Methodology and Requirements

For the proposed design, the adaptive light dimmer must be able to produce the same or more light than a normal 60W incandescent light bulb at maximum brightness. This was determined to be roughly 800 lumens. If the adaptive light dimmer is incapable of producing the appropriate amount of light, a house may not be bright enough during darker hours of the day. Another important aspect is the efficiency of the device. Having better efficiency leads to less costs in terms of both energy and money. While both are definitely important, reduction in energy costs will likely have a greater impact on those rural areas depending on renewable energy sources, since these energy resources are much more limited. The adaptive light dimmer should have at least an efficiency of 70% at full load.

In addition, the device should consume a relatively low amount of power. Considering that the LED being used is a 5 W rated LED, the maximum output power must be 5 W. Derived from the efficiency requirement, the maximum input power must be 7.14 W. The device must be

able to operate from a 48 V input, for example to match the DC bus used by the DC house project [24]. It should ideally be able to operate at other DC voltages as well for other applications. The device must also be quick to respond without causing abrupt changes. So, the device should be able to adjust its output at least once every two seconds. Each adjustment may need to be gradual for any sudden changes in the surrounding brightness levels.

The sensor should be sensitive enough that it can differentiate changes in daylight throughout the day. The sensor measures light intensity in lux, so the placement of the sensor can have a significant effect on measurement values. The best place to put the sensor is near the floor, as this minimizes the vertical movement of the sensor. Near the floor, the expected lux measurements range from 35 to 150 lux. The device will be connected to a lamp, so the entire system will be fairly large. Approximately, the height must be 4 to 5 feet, and the base should be about a foot in diameter. The height will also help to spread the light to the entire room.

Figure 2 depicts the block diagram of the design, consisting of specialized blocks each with their individual inputs and outputs (I/O). It specifies the types and connections of these I/Os and how it achieves the desired output to the level 0 block diagram. The system first takes the 48 V DC and sends it through a Low-Dropout Regulator (LDO) to lower the voltage to 5 V so that it may be used to power the microcontroller and photosensitive sensor. The photosensitive sensor communicates via i2c to the microcontroller to give a light intensity reading. The microcontroller then maps this reading to a corresponding pulse width modulated (PWM) control voltage that it outputs to the LED driver. Based on the PWM voltage it receives from the microcontroller, the LED driver outputs appropriate PWM current using the 48 V DC from the bus. The LED is driven by the current output and produces the correct amount of light out.

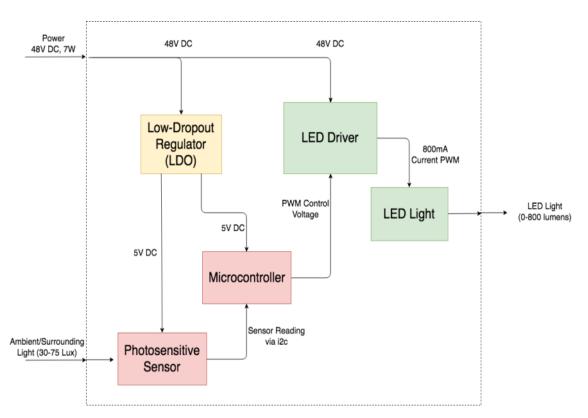


Figure 2. Block diagram of the adaptive light dimmer

The software flow diagram as illustrated in Figure 3 describes the process performed by the microcontroller. The microcontroller initializes the photosensitive sensor and then goes into an endless loop consisting of three stages. First it requests a reading from the sensor. Once it has received and stored this reading, it maps the reading to an appropriate PWM output value through a lookup table. The PWM is then written out to the appropriate pin.

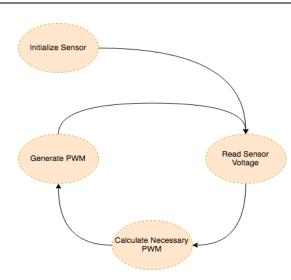


Figure 3. Software flow diagram of the adaptive light dimmer

3. Design and Simulation Results

The main purpose of the Adaptive Light Dimmer is to both minimize costs by using cheaper materials while maximizing efficiency and to operate from a DC voltage source, allowing renewables to be a direct source of power. Simplifying the design can also allow the device to operate without worry of installation. The Adaptive Light Dimmer has three main parts to design. First is the LED driver, which has to drive a 5 V LED at a maximum current of 800 mA. The second is the LDO linear regulator, which had to step down the 48 V input to 5 V in order to power the microcontroller and the light sensor. The last portion of the design is the combination of the microcontroller and the light sensor, both of which have to operate at 5V.

The LED driver used is Linear Technology's LT3795 since it can operate with an input voltage of 48V. The circuit is a buck mode LED driver meant to operate from 24 V to 80 V and output a controlled current to the LEDs. This current can be controlled directly by changing the control voltage or by sending a PWM signal into the PWM pin of the device. The components that has to be changed are the input capacitor C_{IN} , the inductor L, the input current sense resistor $R_{INSENSE}$, the LED current resistor R_{LED} , the PMOS, the NMOS, and the PNP BJT. These components directly affect the operation of the circuit, so they are changed in order to produce the correct maximum output current and to keep the circuit from damaging any components in the design. Because the input voltage was 48 V, during transient operation, the voltage could reach upwards of 63V; thus, requiring careful consideration when designing the circuit. The equation used to find the appropriate value of C_{IN} in microfarads is:

$$C_{IN} = I_{LED} \cdot V_{LED} \cdot \frac{(V_{IN} - V_{LED})}{V_{IN}^2} \cdot T_{SW} \cdot \frac{10\mu F}{A \cdot \mu s}$$

$$C_{IN} = 0.8A \cdot 5V \cdot \frac{(48V - 5V)}{(48V)^2} \cdot \frac{1}{400000 \text{Hz}} \cdot \frac{10\mu F}{A \cdot \mu s} = 1.87\mu F$$
(1)

the equation for sizing the inductor:

$$L = \frac{(T_{SW} \cdot R_{SENSE} \cdot V_{LED} \cdot [V_{IN} - V_{LED}])}{V_{IN} \cdot 0.02V} = \frac{\left(\frac{1}{400000 \text{ Hz}} \cdot 0.015\Omega \cdot 5V \cdot [48V - 5V]\right)}{48V \cdot 0.02V} = 8.4 \mu \text{H}$$
(2)

The input current sense resistor is determined more loosely. Because the configuration is a buck LED driver, the input current should not be very high, so R_{INSNS} is chosen as 100 m Ω . By the equation below, the maximum input current can be determined.

$$I_{\rm IN(MAX)} = \frac{0.06V}{R_{\rm INSNS}} = \frac{0.06V}{0.1\Omega} = 600 \text{ mA}$$
(3)

The LED current resistor is chosen such that the maximum LED current is 800 mA. Because the control pins are tied high, the equation used to determine the LED current resistor is simplified to:

$$R_{LED} = \frac{0.25V}{I_{LED(MAX)}} = \frac{0.25V}{0.8A} = 312.5 \text{ m}\Omega$$
(4)

The PMOS and PNP BJT transistors are chosen differently because the ones from the datasheet are not readily available for both simulation and hardware tests. The same is true for the NMOS transistor, but in addition, the NMOS transistor is chosen to minimize the gate charge. Higher gate charge values increases the current in the INTVCC pin and could disrupt the operation of the circuit by dropping the INTVCC pin below the threshold.

$$Q_{G(MAX)} = \frac{I_{INTVCC}}{f_{OSC}} = \frac{0.02A}{400000Hz} = 50nC$$
(5)

The timing resistor, which determines the switching frequency of the LED driver, is determined to yield 400 kHz. This frequency is neither too low, which would increase component sizing, nor too high, which would increase losses. The feedback resistors, which set the maximum output voltage, are adequate because the LEDs operated at 5 V. The sense resistor R_{SNS} is used to measure the actual current through the LEDs. Its value must be less than 0.07 V/I_{LED}, which for 800 mA, is calculated to be 87.5 m Ω . The final design of the LED driver is shown in Figure 4. Simulations of the design were done using LTSpice. Results are shown in Figures 5 and 6 for the output voltage and the input current at 100% duty cycle, respectively.

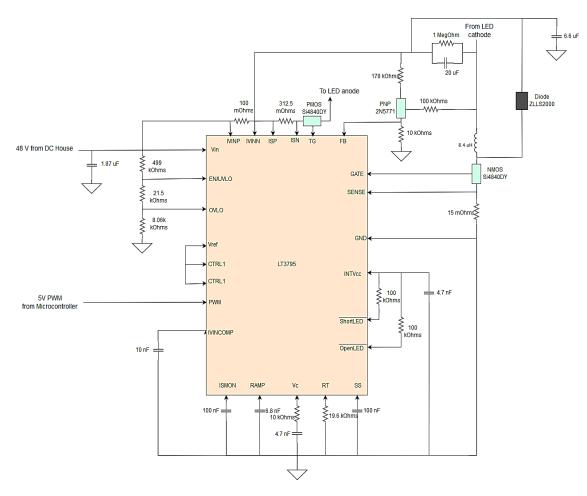


Figure 4. Final schematic of LED driver for the adaptive light dimmer

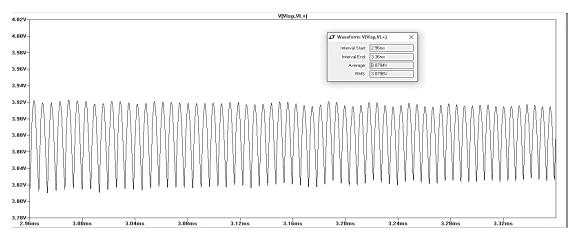


Figure 5. Simulation result for output voltage at 100% duty cycle

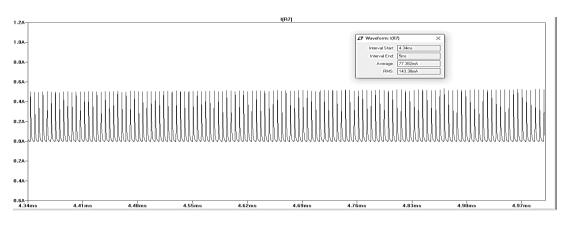
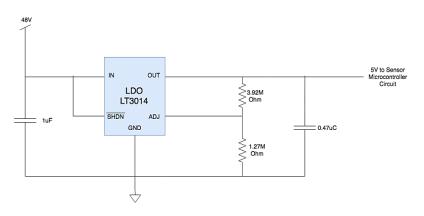
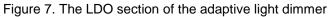


Figure 6. Simulation result for input current at 100% duty cycle

The second portion of the dimmer is the low drop-out regulator (LDO) circuit which lowers the 48 V input to a usable 5V for the microcontroller and sensor. The LT3014 was chosen because it fits well into the design; it has the capability of dropping 48 V to 5 V, and it would incur little power loss since the microcontroller/sensor circuit would not pull a significant amount of current. Figure 7 shows the configuration of the LDO to be implemented taken from the LT3014 datasheet. This configuration is setup for "5 V Supply with Shutdown" according to the datasheet and fits the needs of the light dimmer. The value of the capacitor on the output was specified to be at least 0.47 μ F to prevent oscillations.





The final portion of the dimmer consists of the sensor and microcontroller circuits. The VEML6070 is chosen due to its ability to communicate via I2C and to detect UV light, and because its operating voltage range that is the same as the ATTiny85. The ATTiny85 is chosen for its low cost, I2C capability and ability to produce a PWM output. In addition, it also can be programmed with the Arduino UNO using Sketch's built in "ArduinoISP" library.Since the VEML6070 communicates via I2C, the SDA (data line) and SCL (clock line) have 4.7 k Ω pull up resistors to ensure proper logic levels. These are connected to the 5 V from the LDO, which supplies voltage to the microcontroller and UV light sensor. The output PWM on pin six on the microcontroller connects to the PWM input of the LED Driver. The ACK pin of the VEML6070 sends a signal if the UV light reading drops below a built in threshold and was not connected since the feature is not necessary for the function of the device. The ground is common to the LED driver and LDO circuits as well. The design of the circuit is shown in Figure 8.

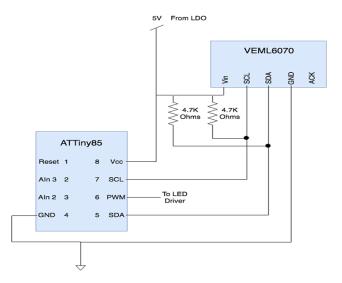


Figure 8. Sensor and Microcontroller Circuit Diagram of the adaptive light dimmer

3. Hardware Construction and Results

The hardware of the light dimmer was built and tested in separate stages. The LED driver, LDO, microcontroller, and sensor were tested individually and then combined to construct the entire system. After constructing the circuit on the PCB as shown in Figure 9, testing the individual components, and checking all possible connections, power was applied and the entire system to evaluate its functionality. Results from the test demonstrate that when dark below 10 Lux, the LED driver received 100% duty cycle and the lamp went to full brightness, and when bright above 100 Lux, the LED driver receives 0% duty cycle and the lamp outputs no light. Different duty cycles were assigned to different ambient light levels in between 0 and 100 Lux. This approach reduces the complexity in the operation of LED driver using the pulse density modulation [25-26].



Figure 9. Adaptive light dimmer hardware prototype

The LED dimmed and grew brighter according to how much light reached the sensor, and the brightness changed smoothly without visible leaps in light output. The adaptive light dimmer worked as expected as ambient light levels increased and increased in brightness during low light. When dark, below 10 Lux, the LED driver received 100% duty cycle and the lamp went to full brightness and when bright, above 100 Lux, the LED driver receiver 0% duty cycle and the lamp outputs no light. Different duty cycles were assigned to different ambient light levels in between 0 and 100 Lux. The LED dimmed and grew brighter according to how much light reached the sensor, and the brightness changed smoothly without visible leaps in light output.Furthermore, the proposed design was quick to respond to changes in the environment, as the LED was able to change more quickly than once every two seconds and each change was gradual. This is important to prevent the LED's dimming from bothering users. Overall, the proposed adaptivelight dimmer is an improvement in functionality compared to the results presented in [2].

However, this achievement comes at the cost of efficiency because the microcontroller and sensor draw about 30 mA, increasing the input power needed while the output power of the LED stays constant. This current is relatively significant compared to 115 mA input current needed for the LED driver. Thus, the microcontroller and sensor increased the input current to yield input power by 26%. The high input voltage of 48 V meant that input power increased by about 1.44 W, about 1.29 W of which was dissipated through the LDO. Figures 10 through 12 detail the hardware test results of the adaptive light dimmer prototype. These results indicate the linear operation of the light dimmer as duty cycle is varied. The efficiency after connecting the microcontroller and LDO was 48.7%, which is well below that presented in [27]. Based on these results, the main function of sensing ambient light levels and dimming appropriately has been achieved by the proposed solution.

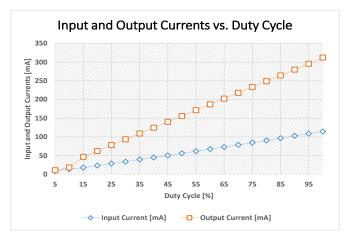


Figure 10. Hardware test results on input and output currents with varying duty cycle

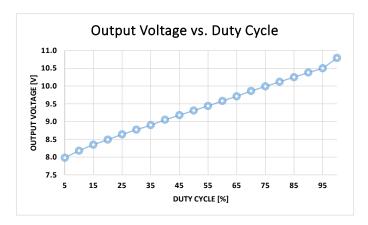


Figure 11. Hardware test results on output voltage with varying duty cycle

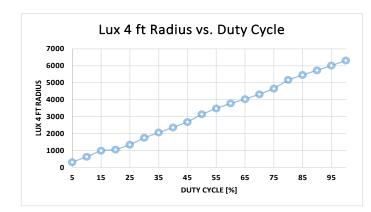


Figure 12. Hardware test results on Lux 4 ft radius with varying duty cycle

There are several ideas to improve the performance of the proposed adaptive light dimmer. One example involves the addition of 0.47 μ F LDO output capacitor will be included on the PCB design to ensure that the microcontroller and sensor circuit were receiving a more stable voltage. This capacitor needs to be connected close to the supply of the microcontroller and sensor. The easiest way to do this would be to connect a 0.47 μ F through-hole ceramic capacitor between the input voltage to the sensor on the PCB and the ground to the sensor on the PCB.

4. Conclusion

An adaptive LED light dimmerwhich operates automatically based on ambient light condition was presented in this paper. The design has been shown through computer simulation and hardware prototype tests to successfully perform the dimming function following the changing condition of the ambient light. Theproposed solution reduces the complexity of previous approaches and it can be adapted to any DC system operating at different DC voltage levels. Further improvements of the proposed solution involves increasing the overall system efficiency. This could be done by utilizing a snubber to the NMOS switch to reduce switching losses at 400 kHz. The PCB design could also be optimized for better signal integrity since many components are farther apart on the current PCB design. Additionally, an alternative to the LDO utilized in the proposed designmust be sought to further improve the overall efficiency.

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